

**VALERIU BEIU** (S'92–M'95–SM'96) received the MSc in Computer Engineering from the University “Politehnica” Bucharest in 1980, and the PhD *summa cum laude* in Electrical Engineering from the Katholieke Universiteit Leuven in 1994.

Since graduating in 1980 he has been with the Research Institute for Computer Techniques, University “Politehnica” Bucharest, Katholieke Universiteit Leuven, King’s College London, Los Alamos National Laboratory, Rose Research, Washington State University, United Arab Emirates University, and currently is with “Aurel Vlaicu” University of Arad.

His research interests have constantly been on biological-inspired nano-circuits and brain-inspired nano-architectures for VLSI-efficient designs (ultra-low power and highly reliable), being funded at over US\$ 51M. On such topics he gave over 200 invited talks, organized over 120 conferences, chaired over 60 sessions, has edited 2 books, and has authored over 230 journal/conference articles (30 invited), as well as 8 chapters and 11 patents.

Dr. Beiu has received 5 fellowships and 7 best paper awards, and is a senior member of the IEEE as well as a member of: ACM, INNS, ENNS, and MCFA. He was a member of the SRC-NNI Working Group on Novel Nano-architectures, the IEEE CS Task Force on Nano-architectures, and the IEEE Emerging Technologies Group on Nanoscale Communications, and has been an Associate Editor of the *IEEE Transactions on Neural Networks* (2005–2008), of the *IEEE Transactions for Very Large Scale Integration Systems* (2011–2015), and of the *Nano Communication Networks* (2010–2015).